

E:/.WORK/STM32/STM
_Libs/STM32_General
/Src/general_uart.c

```
graph TD; A["E:/.WORK/STM32/STM_Libs/STM32_General/Src/general_uart.c"] --> B["general_uart.h"]; A --> C["general_gpio.h"]; B --> D["mylibs_defs.h"]; C --> D;
```

general_uart.h

general_gpio.h

mylibs_defs.h