

E:/.WORK/STM32/STM  
\_Libs/STM32\_General  
/Src/general\_spi.c

```
graph TD; A["E:/.WORK/STM32/STM_Libs/STM32_General/Src/general_spi.c"] --> B["general_spi.h"]; A --> C["general_gpio.h"]; B --> D["mylibs_defs.h"]; C --> D;
```

general\_spi.h

general\_gpio.h

mylibs\_defs.h