

E:/.WORK/STM32/STM32
_ExtendedLibs/MyLibsGeneral
/Inc/general_tim.h

```
graph BT; A["E:/.WORK/STM32/STM32_ExtendedLibs/MyLibsGeneral/Inc/mylibs_include.h"] --> B["E:/.WORK/STM32/STM32_ExtendedLibs/MyLibsGeneral/Inc/general_tim.h"]; C["E:/.WORK/STM32/STM32_ExtendedLibs/MyLibsGeneral/Src/general_tim.c"] --> B;
```

E:/.WORK/STM32/STM32
_ExtendedLibs/MyLibsGeneral
/Inc/mylibs_include.h

E:/.WORK/STM32/STM32
_ExtendedLibs/MyLibsGeneral
/Src/general_tim.c